

Federico Corradi, Ph.D.

POSTDOC UNIVERSITY OF ZÜRICH, R&D SCIENTIST INILABS LTD. · NEUROMORPHIC ENGINEERING EXPERT

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Academic Experience

PostDoc Researcher, Neuromorphic Cognitive Systems Group

Zürich, Switzerland

UNIVERSITY OF ZÜRICH

Jun. 2016 - now

- Working towards a new generation of brain-inspired computing platforms, i.e. neuromorphic processors. Development and implementation of deep neural networks algorithms in neuromorphic hardware. Exploring deep neural networks algorithms for visual pattern identification and classification. Development of neural network architectures tailored to neuromorphic hardware (processors, silicon retinas, and silicon cochleas).

Undergraduate Researcher, Neuromorphic Cognitive Systems Group

Zürich, Switzerland

UNIVERSITY OF ZÜRICH

Mar. 2011 - Jul. 2015

- Worked for the EU FP7 ERC project Neurop 257219 project. One of the main goal was to build event-based VLSI models of cortical circuits for brain-inspired computation.
- Developed a variety of mixed-signal analog/digital VLSI neuromorphic devices (Spikebetter, ROLLS, Ziggi) for emulating basic processes and functions of neural spiking systems.
- Realized a neuromorphic system capable of recording from neural tissue. This system is conceived as event-based Brain-Machine Interface that exploits asynchronous logic to sense and transmit information collected from neural tissue

Education

University of Zürich and ETH Zürich

Zürich, Switzerland

PH.D. IN NEUROSCIENCE AND NATURAL SCIENCES

2011 - 2015

My Ph.D. has focused at the interface between neuroscience and neuromorphic engineering. I've been investigating basic research questions that are related to the way neural circuits carry out computation and at the same time in developing a new generation of computing technologies based on neuromorphic circuits on CMOS VLSI technology. The title of my Ph.D. dissertation is "Distributed Information Processing in Neural-Inspired Microelectronic Circuits". In addition, I have successfully completed the international Ph.D. program in Neuroscience offered by the UZH and ETH.

Università La Sapienza

Rome, Italy

M.Sc. IN PHYSICS

2007 - 2010

MSc Thesis at the Italian Institute of Health (ISS) under the supervision of Prof. P. del Giudice and Prof. L. Zanello. I've been working in the computational neuroscience field researching on neuromorphic hardware. The title of my MSc thesis is "Attractor Dynamics in a Network of Spiking Neurons on VLSI Neuromorphic Chips"

Universiteit Leiden

Leiden, The Netherlands

INTERN IN PHYSICS LABORATORY

2005 - 2006

Worked in the MiniGRAIL team that is part of the Quantum Physics and Applications at Ultra Low Temperatures in the Lion Laboratory under the supervision of Prof. Giorgio Frossati. I participated, as exchange student during the one year Erasmus program, in testing and developing electronic instruments at low temperature for the gravitational radiational antenna. Provided research for the BSc Thesis that has been presented in Parma University.

Università degli studi di Parma

Parma, Italy

B.Sc. IN PHYSICS

2003 - 2007

Title of my BSc thesis "Study of High Quality Factor Capacitive Transducers, for Gravitational Antennas".

Teaching Experience

UZH Teaching Assistant

Zürich, Switzerland

UNIVERSITÄT ZÜRICH

Fall 2012

- Neuromorphic Engineering I

UZH Teaching Assistant

UNIVERSITÄT ZÜRICH

- Neuromorphic Engineering II

Zürich, Switzerland

Spring 2012

ETH Teaching Assistant

EIDGENÖSSISCHE TECHNISCHE HOCHSCHULE ZÜRICH

- Laboratory Electronics: Analog Circuit Design

Zürich, Switzerland

Fall 2011

ETH Teaching Assistant

EIDGENÖSSISCHE TECHNISCHE HOCHSCHULE ZÜRICH

- Physics Laboratory

Zürich, Switzerland

Spring 2011

Unix Instructor

PERFORMA S.R.L

- Unix System Administrator

Rome, Italy

2009/2010

Employments

University of Zürich and ETH Zürich

POSTDOC RESEARCHER

50% position in Neuromorphic Engineering, Neuroscience. Development of Neural Network algorithms for embedded systems. Development of neuromorphic hardware.

Zürich, Switzerland

2016 - now

Inilabs GmbH

RESEARCH & DEVELOPMENT SCIENTIST

- Inilabs develops and sells neuromorphic technologies.
- Characterization of a variety of Dynamic Vision Sensors, DAVIS and DVS.
- Involved in the Neuromorphic Processor Project (NPP) that is sponsored by Samsung. One of the main goals is to develop a digital deep neural network accelerator. Specifically, I have been working on developing a Convolutional Neural Network for face detection. This CNN exploits the sparse output data from the silicon retina (DVS).
- I am currently involved in a project that aims to achieve 3D scene reconstruction using an active vision system composed of an event-based dynamic vision sensor (DVS) and a laser.
- I am currently involved in a project that aims to achieve real-time tracking of moving objects in the environment. This project targets robotic industrial applications.

Zürich, Switzerland

2015 - now

Flyer Communication

SYSTEM ADMINISTRATOR

Unix System administrator for a web-based company.

Rome, Italy

2009 - now

University of Zürich and ETH Zürich

PH.D. STUDENT

Ph.D. in Neuromorphic Engineering, Neuroscience.

Zürich, Switzerland

2011 - 2015

Xoolab

WEBSITE DEVELOPER

Web developer in Php, Mysql, Ajax.

Rome, Italy

2008/2009

Immobiliare.it

WEBSITE DEVELOPER

Web developer in Php, Mysql, Ajax.

Rome, Italy

2008

Extracurricular Activity

Peer Reviewer IEEE Transactions on Circuits and Systems

INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, IEEE

- Reviewed multiple papers on circuits and systems.

Online

Sep. 2015 - PRESENT

Peer Reviewer IEEE Transaction Neural Networks and Learning Systems

INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, IEEE

- Reviewed multiple papers on neuromorphic engineering and learning systems.

Online

Sep. 2012 - PRESENT

Peer Reviewer Frontiers in Neuromorphic Engineering

FRONTIERS IN NEUROSCIENCE

- Reviewed multiple papers on neuromorphic engineering and learning systems.

Online

Sep. 2015 - PRESENT

IEEE Computational Intelligence Society

MEMBER

- Gained expertise computational intelligence and learning systems.
- Participated on several IEEE conferences and won an award.

Online

Sep. 2014 - PRESENT

Honors & Awards

INTERNATIONAL

- 2014 **Honorary Mention**, For the manuscript entitled "Mapping Arbitrary Mathematical Functions and Dynamical Systems to Neuromorphic VLSI Circuits for Spike-based Neural Computation", by the Neural Systems and Applications Circuits and Systems Technical Committee *ISCAS, Melbourne NSA Track*

Certification

Laser Safety Officer for technical applications

UVEX ACADEMY

- The title fulfills the requirements of the German D6UV rule 11 (hitherto BGV B2) 'Accident prevention regulation for laser beam'.

Zürich

Feb. 2016

Peer-reviewed journal papers

A Neuromorphic Event-Based Neural Recording System for Smart Brain-Machine-Interfaces

F. CORRADI, AND G. INDIVERI

doi:10.1109/TBCAS.2015.2479256

Biomedical Circuits and Systems, IEEE Transactions on, Vol. 99

2015

Real time unsupervised learning of visual stimuli in neuromorphic VLSI systems

M. GIULIONI, F. CORRADI, V. DANTE, AND P. DEL GIUDICE

doi:10.1038/srep14730

Nature Scientific Reports, Vol. 5

2015

A Re-configurable On-line Learning Spiking Neuromorphic Processor comprising 256 neurons and 128K synapses

N. QIAO, H. MOSTAFA, F. CORRADI, M. OSSWALD, F. STEFANINI, D. SUMISLAWSKA, AND G. INDIVERI

doi:10.3389/fnins.2015.00141

Frontiers in Neuroscience, Vol. 9

2015

Towards a Neuromorphic Vestibular System

F. CORRADI, D. ZAMBRANO, M. RAGLIANTI, G. PASSETTI, C. LASCHI, AND G. INDIVERI

doi:10.1109/TBCAS.2014.2358493

Biomedical Circuits and Systems, IEEE Transactions on, Vol. 8

2014

Peer-reviewed conference papers

Steering a Predator Robot using a Mixed Frame/Event-Driven Convolutional Neural Network

P.M. DIEDERIK, F. CORRADI, E. KERR, P. VANCE, G. DAS, D. NEIL, D. KERR, TOBI DELBRÜCK

IEEE Event-Based Control, Communication and Signal Processing

2016, Krakow, POL

Decision Making and Perceptual Bistability in Spike-Based Neuromorphic VLSI Systems

F. CORRADI, H. YOU, M. GIULIONI, G. INDIVERI

doi:10.1109/ISCAS.2015.7169245

IEEE International Symposium on Circuits and Systems

2015, Lisbon, PT

Neuromorphic Architectures for Spiking Deep Neural Networks

G. INDIVERI, F. CORRADI, AND N. QIAO

doi:10.1109/IEDM.2015.7409623

IEEE International Electron Devices Meeting

2015, Washington, DC, USA

Toward Neuromorphic Intelligent Brain-machine Interfaces: an Event-based Neural Recording and Processing System

F. CORRADI, D. BONTRAGER, AND G. INDIVERI

doi:10.1109/BioCAS.2014.6981793

IEEE Biomedical Circuits and Systems Conference

2014, Lausanne, CH

Mapping Arbitrary Mathematical Functions and Dynamical Systems to Neuromorphic VLSI Circuits for Spike-Based Neural Computation

F. CORRADI, C. ELIASMITH, AND G. INDIVERI

doi:10.1109/ISCAS.2014.6865117

IEEE International Symposium on Circuits and Systems

2014, Melbourne, VIC, AU

A spiking implementation of the lamprey's Central Pattern Generator in neuromorphic VLSI

E. DONATI, F. CORRADI, C. STEFANINI, G. INDIVERI

doi:10.1109/BioCAS.2014.6981775

IEEE Biomedical Circuits and Systems Conference

2014, Lausanne, CH

A Hybrid Analog/Digital Spike-Timing Dependent Plasticity Learning Circuit for Neuromorphic VLSI Multi-Neuron Architectures

H. MOSTAFA, F. CORRADI, F. STEFANINI, AND G. INDIVERI

doi:10.1109/ISCAS.2014.6865270

IEEE International Symposium on Circuits and Systems

2014, Melbourne, VIC, AU

Automated synthesis of asynchronous event-based interfaces for neuromorphic systems

H. MOSTAFA, F. CORRADI, M. OSSWALD, AND G. INDIVERI

doi:10.1109/ECCTD.2013.6662213

IEEE European Conference on Circuit Theory and Design

2013, Dresden, DE

Implementation of a Neuromorphic Vestibular Sensor with Analog VLSI Neurons

G. PASSETTI, F. CORRADI, M. RAGLIANTI, D. ZAMBRANO, C. LASCHI, AND G. INDIVERI

doi:10.1109/BioCAS.2013.6679667

IEEE Biomedical Circuits and Systems Conference

2013, Rotterdam, NL

Demonstrations at international conferences

Learning to recognize visual stimuli in neuromorphic VLSI

F. CORRADI, M. GIULIONI

doi:10.1109/BioCAS.2012.6418494

IEEE Biomedical Circuits and Systems Conference

2012, Hsinchu, TW

Skills

VLSI design of mixed signal analog/digital circuits

Cadence, Spice

Printed circuit board design

Altium, Eagle

Software design

C/C++, Python, Java

Neural network simulation of deep and/or spiking networks

Caffe Deep Learning Framework, Brian, Matlab

Dynamical System Analysis

Xppaut, Mathematica

Linux system administration of web-servers and always-on systems

Linux, Unix, MySQL, Apache2, Bash

Web design

Php, Nodejs, MongoDB, Ajax, Javascript, Css

Langages

Italian (mother tongue), English (fluent)